

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: M. HASEGAWA, et al.

Application No.: Rule 1.53(b) Continuation of U.S. Patent Application
Serial No. 09/811,400, filed March 20, 2001

Filed: On even date herewith

For: A SEMICONDUCTOR MEMORY DEVICE HAVING
IMPROVED ARRANGEMENT FOR REPLACING FAILED
BIT LINES (As Amended)

Art Group of Parent: 2818

Examiner of Parent: C. C. Yoha

PRELIMINARY AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

July 28, 2003

Sir:

Prior to examination, please amend the above-identified application as follows:

IN THE TITLE:

Please amend the Title as follows:

A SEMICONDUCTOR MEMORY DEVICE HAVING IMPROVED
ARRANGEMENT FOR REPLACING FAILED BIT LINES

SPECIFICATION:

On page 1, before the heading "BACKGROUND OF THE INVENTION,"
please insert the following heading and paragraph: